



Nanometer CMOS RFICs for Mobile TV Applications (Analog Circuits and Signal Processing)

Ahmed A. Youssef, James Haslett

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Nanometer CMOS RFICs for Mobile TV Applications focuses on how to break the trade-off between power consumption and performance (linearity and noise figure) by optimizing the mobile TV front-end dynamic range in three hierarchical levels: the intrinsic MOSFET level, the circuit level, and the architectural level. It begins by discussing the fundamental concepts of MOSFET dynamic range, including nonlinearity and noise. It then moves to the circuit level introducing the challenges associated with designing wide-dynamic range, variable-gain, broadband low-noise amplifiers (LNAs). The book gives a detailed analysis of a new noise-canceling technique that helps CMOS LNAs achieve a sub - 2 dB wideband noise figure. Lastly, the book deals with the front-end dynamic range optimization process from the systems perspective by introducing the active and passive automatic gain control (AGC) mechanism.



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